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PATENTS, TRADEMARKS
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FACSIMILE MESSAGE

TO: Examiner Hau H. Nguyen Art Unit 2676
COMPANY: U.S. Patent and Trademark Office
RE: Serial No.: 09/060,771 - Filed: September 21, 2001
FILE NO.: 00-335 / 1496-00154
FAX NO.: (703) 872-9306
FROM: John J. Ignatowski, Esq.
DATE: March 11, 2004 TIME: _____

TOTAL NUMBER OF PAGES 4 *(including cover sheet)*

If you do not receive any of these pages, please telephone us at (586) 498-0670 or telefax us at (586) 498-0673.

COMMENTS:

Enclosed is:

Applicant Initiated Interview Request Form (3 pages).

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PTOL-413A (03-03)

Approved for use through 07/31/2006, OMIS 0031-0031
U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Applicant Initiated Interview Request Form

Application No.: 09/160,771 First Named Applicant: David N. Porter
 Examiner: Nguyen, Hoa M. Art Unit: 2676 Status of Application: First rejection

Tentative Participants:

(1) Examiner Nguyen (2) John J. Tognarelli
 (3) _____ (4) _____

Proposed Date of Interview: 3/18/04 Proposed Time: 1:30 (AM/PM) (M)

Type of Interview Requested:

(1) ☒ Telephonic (2) ☐ Personal (3) ☐ Video Conference

Exhibit To Be Shown or Demonstrated: ☐ YES

NO

If yes, provide brief description: _____

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Issues To Be Discussed

Issues (Itej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) <u>Rej.</u>	<u>1-3, 4, 13, 15, 16, 21, 22</u>	<u>Zhang et al.</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(2) <u>Rej.</u>	<u>1, 4, 5, 12-15</u>	<u>Porter</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(3) <u>Rej.</u>	<u>1, 3, 8, 12, 16, 17</u>	<u>Morton et al.</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(4) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☒ Continuation Sheet Attached

Brief Description of Arguments to be Presented:

See attached Continuation Sheets (new pages)

An interview was conducted on the above-identified application on _____.

NOTE:

This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

(Applicant/Applicant's Representative Signature)

(Examiner/SIP Signature)

This collection of information is required by 37 CFR 1.133. The information is required to obtain or certain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 21 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22304-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Applicant Initiated Interview Request Form Continuation Sheet

Appl. No.: 09/960,771
Applicant: David N. Pether
Examiner: Nguyen, H.
Art Unit: 2676

Zhang

Claims 1, 13 and 22

- 1) Where does Zhang disclose a first input data stream having a first image?
- 2) Where does Zhang disclose Modification Units 404-408 modifying the first image?
- 3) Where does Zhang disclose a second input data stream?
- 4) Where does Zhang disclose Stream Bit Multiplexer 526 performing a spatial combination?

Claim 2

Where does Zhang disclose a block modify and move engine?

Claim 3

Where does Zhang disclose an image in the second input data stream?

Claims 9 and 18

Where does Zhang disclose scaling of an image?

Claims 11 and 19

Where does Zhang disclose that the Stream Bit Multiplexer 526 performs a bitwise logical operation on the first output data stream and the second output data stream?

Claim 21

- 1) Where does Zhang disclose a memory configured to buffer a first data, a second data and a third data?
- 2) Where does Zhang disclose a first expander to generate the first input data stream?
- 3) Where does Zhang disclose a second expander to generate the second input data stream?
- 4) Where does Zhang disclose the claimed third control signal?
- 5) Where does Zhang disclose a third expander to generate the control signal?

Porter

Claims 1 and 13

- 1) Where does Porter disclose a first input data stream having a first image?
- 2) Where does Porter disclose the memory 10 modifying the first image?

Claims 4 and 14

- 1) Where does Porter disclose the memory 10 converting an input format of a first input data stream between a video data format and a graphics data format?
- 2) Where does Porter disclose the memory 10 converting an output format of a first output data stream between a video data format and a graphics data format?

Appl. No.: 09/960,771

Claim 5

Where does Porter disclose placing video pictures in front of graphics data?

Claim 15

Where does Porter disclose placing graphics data in front of video images?

Moreton

Claims 1 and 13

- 1) Where does Moreton disclose modification of a first image in a first input data stream?
- 2) What block(s) in FIG. 4 disclose the claimed first data modification circuit?
- 2) Where does Moreton disclose a second output data stream?
- 3) Where does Moreton disclose the multiplexer of FIG. 4 performing a spatial combination?

Claims 7 and 16

Where does Moreton disclose that the blocks in FIG. 4 (similar to the first data modification circuit) interleave color components in the first image?

Claims 8 and 17

Where does Moreton disclose that the blocks in FIG. 4 (similar to the first data modification circuit) perform separation of color components in the first image?